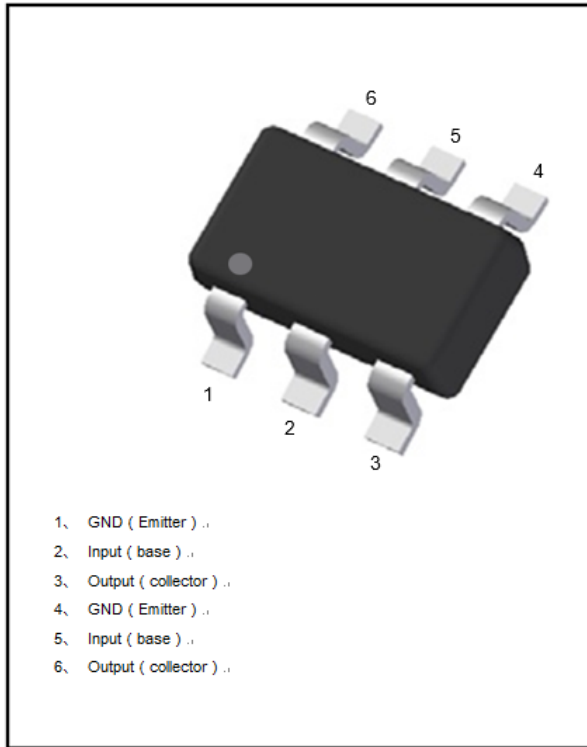


Dual NPN Digital Transistors (Built-in Resistors)

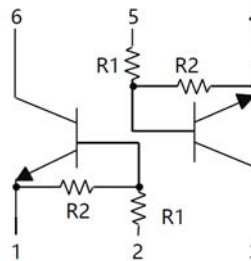


Features

- Epoxy meets UL-94 V-0 flammability rating
- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors
- Surface mount package ideally Suited for Automatic Insertion

Mechanical Data

- **Package:** SOT-363S
- **Terminals:** Tin plated leads, solderable per J-STD-002 and JESD22-B102
- **Marking:** H24
- **Equivalent circuit**



■Maximum Ratings (Ta=25°C Unless otherwise specified)

ITEM	SYMBOL	UNIT	CONDITIONS	VALUE
Supply Voltage	V_{CC}	V		50
Input Voltage	V_{IN}	V		-10 to +40
Output Current	I_O	mA		100
Power Dissipation	P_D	mW		200
Junction Temperature (Single)	T_J	°C		150
Storage Temperature	T_{STG}	°C		-55 to +150



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■ Electrical Characteristics (Ta=25°C unless otherwise specified)

ITEM	SYMBOL	UNIT	CONDITIONS	MIN	TYP	MAX
Input voltage	$V_{I(off)}$	V	$V_{CC}=5V, I_c=100\mu A$	0.5	-	-
	$V_{I(on)}$	V	$V_O=0.3V, I_c=1mA$	-	-	3
Output voltage	$V_{O(on)}$	V	$I_o / I_i = 5mA / 0.25 mA$	-	-	0.3
Input current	I_i	mA	$V_i=5V$	-	-	0.15
Output current	$I_{O(off)}$	uA	$V_{CC}=50V, V_i=0$	-	-	0.5
DC current gain	G_i		$V_o=5V, I_o = 5mA$	82	-	-
Input resistance	R_i	kΩ		70	100	130
Resistance ratio	R_2/R_1			0.8	1	1.2
Transition frequency	f_T	MHz	$V_{CE}=10V, I_E=5mA, f=100MHz$	-	250	-

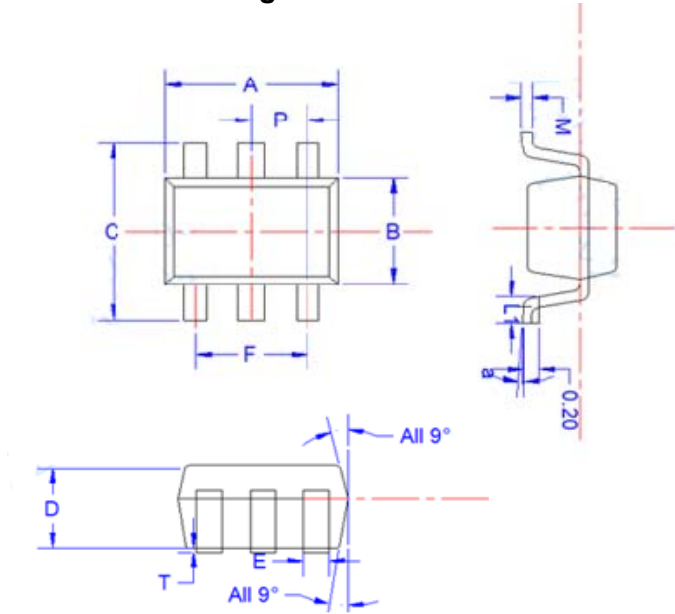
■ Ordering Information (Example)

PREFERRED P/N	PACKING CODE	UNIT WEIGHT(g)	MINIMUM PACKAGE(pcs)	INNER BOX QUANTITY(pcs)	OUTER CARTON QUANTITY(pcs)	DELIVERY MODE
UMH24NS	F2	Approximate 0.009g	3000	30000	120000	7" reel



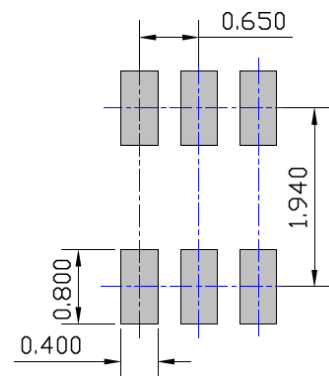
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■SOT-363S Package Outline Dimensions



SYMBOL	MILLIMETER		
	MIN	NOM	MAX
E	0.15	0.25	0.35
B	1.15	1.25	1.35
C	2.00	2.10	2.20
P	0.650BSC		
A	1.80	2.00	2.20
T	0.00	0.05	0.100
D	0.90	0.95	1.00
L1	0.20	0.30	0.40
a	4°±4°		
M	0.10	0.15	0.25

■Suggested Pad Layout



Unit: mm



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